

WEST Search History

DATE: Friday, April 28, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L44	L43	1
<input type="checkbox"/>	L43	l1 and user\$	1
<input type="checkbox"/>	L42	l35 not l41	10
<input type="checkbox"/>	L41	l35 and debug\$	11
<input type="checkbox"/>	L40	7010672[pn]	1
<input type="checkbox"/>	L39	l37 and code	1
<input type="checkbox"/>	L38	L37 and (hdl or "hardware description language" or verilog or vhdl or language)	0
<input type="checkbox"/>	L37	20040250164	1
<input type="checkbox"/>	L36	20040250164 L18 and L4	11
<input type="checkbox"/>	L35	l33 and trigger\$	21
<input type="checkbox"/>	L34	l31 and trigger\$	115
<input type="checkbox"/>	L33	(compil\$ with (hdl or "hardware description language" or verilog or vhdl)) same ((rtl or "register transfer level") with (netlist or "net list"))	72
<input type="checkbox"/>	L32	L31 and l2	213
<input type="checkbox"/>	L31	(hdl or "hardware description language" or verilog or vhdl) same ((rtl or "register transfer level") with (netlist or "net list"))	431
<input type="checkbox"/>	L30	(rtl or "register transfer level") with (netlist or "net list")	796
<input type="checkbox"/>	L29	L28	59
<input type="checkbox"/>	L28	L27 and (user\$ or customiz\$)	59
<input type="checkbox"/>	L27	(hdl or "hardware description language" or verilog or vhdl) with trigger\$	78
<input type="checkbox"/>	L26	L25 and L2	78
<input type="checkbox"/>	L25	L24	27517
<input type="checkbox"/>	L24	trigger\$ with user\$	27517
<input type="checkbox"/>	L23	L18 and L4	11
<input type="checkbox"/>	L22	L18 and L3	6
<input type="checkbox"/>	L21	L18 and L2	1
<input type="checkbox"/>	L20	customiz\$ with (trigger\$ near1 circuit)	0
<input type="checkbox"/>	L19	customiz\$ with (trigger\$ near1 logic)	3
<input type="checkbox"/>	L18	L17 or L14	236
<input type="checkbox"/>	L17	L16	187

<input type="checkbox"/>	L16	user\$ with (trigger\$ near1 circuit)	187
<input type="checkbox"/>	L15	L14	53
<input type="checkbox"/>	L14	user\$ with (trigger\$ near1 logic)	53
<input type="checkbox"/>	L13	L10 and L4	430
<input type="checkbox"/>	L12	L10 and L3	79
<input type="checkbox"/>	L11	L10 and L2	60
<input type="checkbox"/>	L10	L9 or L5	21674
<input type="checkbox"/>	L9	trigger\$ near1 circuit	20087
<input type="checkbox"/>	L8	L5 and L4	140
<input type="checkbox"/>	L7	L5 and L3	43
<input type="checkbox"/>	L6	L5 and L2	34
<input type="checkbox"/>	L5	trigger\$ near1 logic	1868
<input type="checkbox"/>	L4	714/\$(ccls]	35829
<input type="checkbox"/>	L3	703/\$(ccls]	8875
<input type="checkbox"/>	L2	716/\$(ccls]	9901
<input type="checkbox"/>	L1	6581191[pn]	1

END OF SEARCH HISTORY

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		<i>DB=PGPB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L3	L2 and trigger\$[clm]	5
<input type="checkbox"/>	L2	("user-defined" with logic)[clm]	28
<input type="checkbox"/>	L1	("user-defined" with trigger\$)[clm]	23

END OF SEARCH HISTORY